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P Attorney Docket No. 5649-1154

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Serial No.: 10/774,902 Filed: February 9, 2004

For: INTEGRATED CIRCUIT MEMORY DEVICES HAVING ZIG-ZAG

ARRANGEMENTS OF COLUMN SELECT IO BLOCKS TO INCREASE

INPUT/OUTPUT ROUTING EFFICIENCY

Date: May 17, 2004

Office of Patent Legal Administration Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

PETITION TO ACCORD THE PROPER FILING DATE

Sir:

As set forth in 37 CFR § 1.10(c) and MPEP § 506.02, Applicant hereby petitions to grant the proper filing date of <u>February 9, 2004</u> rather than <u>February 10, 2004</u> for the above-identified patent application as this was the date the application was deposited as "Express Mail" in the U.S. Postal Service. The following is enclosed as evidence of <u>February 9, 2004</u> being the correct filing date for the application, which was properly filed under 37 C.F.R. § 1.10:

- 1. A true and accurate copy of the Cover Sheet for the Application showing the Express Mail signature and date on Page 2;
- 2. A true and accurate copy of the Express Mail Receipt along with a copy of the USPS-Track & Confirm from the web page (<u>www.usps.com</u>), showing "acceptance" or deposit date with the USPS at 4:31 p.m. on <u>February 9, 2004</u>; and
- 3. A true and accurate copy of our return post card and of the official filing receipt for this application, sent by the United States Patent and Trademark Office, showing that Applicant was improperly given the filing date of February 10, 2004 instead of the Express Mail date of February 9, 2004.

This petition is being filed promptly after Applicant became aware of the improperly accorded filing date. Accordingly, Applicant respectfully requests the granting of February 9, 2004 as the filing date pursuant to 37 CFR § 1.10.

In re: Hyun-Seok Lee et al. Serial No.: 10/774,902

Filed: February 9, 2004

Page 2

On the basis that Applicant properly followed the requirements of 37 CFR § 1.10 and the USPTO failed to accord the proper filing date under MPEP § 505 and thus the need to file this petition is due to error on the part of the USPTO, it is applicant's belief that no fee is due with this petition. However, the Commissioner is authorized to charge any deficiency or credit any overpayment to Deposit Account No. 50-0220.

Respectfully submitted

Grant // Scott

Attorney For Applicant Registration No. 40,723

USPTO Customer No. 20792

Myers Bigel Sibley & Sajovec Post Office Box 37428

Raleigh, North Carolina 27627

Telephone: 919/854-1400 Facsimile: 919/854-1401

Certificate of Mailing under 37 CFR 1.8

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Office of Patent Legal Administration, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on May 17, 2004.

Candi I Riggs

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE ATTY DOCKET NO.: 5649-1154

DATE: February 9, 2004

UTILITY PATENT APPLICATION TRANSMITTAL LETTER AND FEE TRANSMITTAL FORM (37 CFR § 1.53(b))

Mail Stop PATENT APPLICATION Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 Sir: Transmitted herewith for filing under 37 CFR § 1.53(b) is: a patent application a Continuation a Divisional a Continuation-in-Part (CIP) Application of prior application no.: _; filed _____, the disclosure of which is incorporated herein by reference in its entirety. and claims the benefit of Provisional Application No. Inventor(s) or Application Identifier: Hyun-Seok Lee - Seoul, Republic of Korea Kyung-Ho Kim - Gyeonggi-do, Republic of Korea Hyeun-Su Kim - Gyeonggi-do, Republic of Korea INTEGRATED CIRCUIT MEMORY DEVICES HAVING ZIG-ZAG Entitled: ARRANGEMENTS OF COLUMN SELECT IO BLOCKS TO INCREASE INPUT/OUTPUT ROUTING EFFICIENCY Myers Bigel Sibley & Sajovec, P.A. P. O. Box 37428 Raleigh, North Carolina 27627 Telephone: (919) 854-1400 Facsimile: (919) 854-1401 Customer No. 20792 Enclosed are: 1. Application Transmittal Letter and Fee Transmittal Form (A duplicate is enclosed for fee processing) 2. \(\square\) 17 pages of Specification (including 20 claims) 3. 10 sheets of Formal Drawings (35 USC 113) 4. Oath or Declaration a. mewly executed (original or copy) copy from prior application (37 CFR 1.63(d) (for continuation/divisional) [Note Box 6 Below] DELETION OF INVENTOR(S) (Signed statement deleting inventor(s) named in the prior application) Application Data Sheet. See 37 CFR 1.76 Incorporation By Reference (useable if box 4b is checked) The entire disclosure of the prior application, from which a copy of the oath or declaration is supplied under Box 4b, is considered as being part of the disclosure of the accompanying application and is hereby incorporated

by reference therein.

8. Assignment page 9. Applicant clair 10. Information Di 11. Preliminary Ar 12. English Transl 13. Certified Copy 14. Sequence Listi a. Computer rea b. paper copy c. statement in statemen	ation Document of Korean Applicang/ Sequence List dable copy support Power of Attorney Postcard (MPEP Ventors(s) (Signed state of Request under 3.	and document(s)) atus. See 37 CFF at and PTO-1449 a enter all claim cation No. 10-20 ing Diskette 503) (Should be specified attached deleting is 5 U.S.C. 122(b)(celow:	amendments prior 03-0039226; Filed pecifically itemized) nventor(s) name in the prior (2)(B)(i). Applicant must	application. See 37 CFR 1.63(d)(: st attach form.	
	Column 1 No. Filed	Column 2 No. Extra	Small Entity Rate Fee	Large Entity Rate Fee	,
BASIC FEE			\$385.00	\$ 770.00	
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Commissions for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

MULTIPLE Dependent Claims Presented

INDEP CLAIMS





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112-19-04

GJS:clr³²²⁷⁸² U.S. PTC AU9: 13**10/774902** -

5649-1154 February 9, 2004

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Sir:

Kindly acknowledge receipt of the accompanying by placing your receiving date stamp hereon and mailing:

Utility Patent Application Transmittal (in duplicate); Specification (17 pages-20 claims); Drawings (10 sheets-Figures 1-8); Declaration and Power of Attorney; Assignment Recordation Form Cover Sheet; Assignment; Information Disclosure Statement; Form PTO-1449; Submittal of Priority Document; Certified Copy of Korean Appl. No. 10-2003-0039226; Check for \$1,608.00; In re: Hyun-Seok Lee et al.; For: INTEGRATED CIRCUIT MEMORY DEVICES HAVING ZIG-ZAG ARRANGEMENTS OF COLUMN SELECT IO BLOCKS TO INCREASE INPUT/OUTPUT ROUTING EFFICIENCY

Respectfully submitted, MYERS BIGEL SIBLEY & SAJOVEC

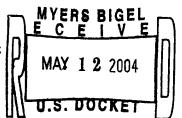




UNITED STATES DEPARTMENT OF COMMERCE UNITED STATES DEPARTMENT OF COMMI United States Patent and Trademark Office Address COMMISSIONER FOR PATENTS PC Box 1450 Alexandria, Vignus 22313-1450 www.toplu.gov

FILING OR 371 APPL NO. ART UNIT FIL FEE REC'D ATTY.DOCKET NO **DRAWINGS** TOT CLMS IND CLMS (c) DATE 10/774,902 02/10/2004 2818 1028 5649-1154 10 20 6

20792 MYERS BIGEL SIBLEY & SAJOVEC PO BOX 37428 RALEIGH, NC 27627



CONFIRMATION NO. 8775

FILING RECEIPT OC000000012577436

Date Mailed: 05/10/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Hyun-Seok Lee, Seoul, KOREA, REPUBLIC OF; Kyung-Ho Kim, Gyeonggi-do, KOREA, REPUBLIC OF; Hyeun-Su Kim, Gyeonggi-do, KOREA, REPUBLIC OF:

Domestic Priority data as claimed by applicant

Foreign Applications

REPUBLIC OF KOREA 2003-39226 06/17/2003

If Required, Foreign Filing License Granted: 05/08/2004

Projected Publication Date: 12/23/2004

Non-Publication Request: No

Early Publication Request: No

05-12-04 A09:13 IN

Title

Integrated circuit memory devices having zig-zag arrangements of column select IO blocks to increase input/output line routing efficiency

LICENSE FOR FOREIGN FILING UNDER Title 35, United States Code, Section 184 Title 37, Code of Federal Regulations, 5.11 & 5.15

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